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PATENT ABSTRACTS OF JAPAN

(74) Representative:

(57) Abstract:

CONSTITUTION: A control voltage from a D/A converter 41 during AFC operation is fed to a VCXO 42 via an LPF 62 with a small time constant. Thus, since the control voltage is fed to the VCXO 42 without a large delay, quick AFC operation is attained. When a reception channel frequency is tentatively changed due to the effect of fading during talking, a frequency comparator circuit 22 detects a frequency difference between a reception channel frequency and an oscillating frequency of the VCXO 42. A control voltage to correct the frequency difference to zero is outputted from a main body 21. Control voltage information is processed into an analog signal and a change component is obtained an LPF 61 having a large time constant and the result is fed to the VCXO 42. Thus, even when the reception channel frequency is tentatively changed due to fading, the transmission channel frequency receives no effect and is made stable.

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